

WE CLAIM:

1. A system for modeling an electronic structure, comprising:

5       an input data generator operable to generate input data comprising information describing a plurality of characteristics of a structure to be modeled, wherein said plurality of characteristics comprise a plurality of segments, subdivisions, and compositions;

10       a segmentation generator, coupled to said input data generator, operable to select segments of said structure to be modeled, to organize the sequence of said segments, to store the data related to said sequenced segments into a segment file, and to convert said segment file into a format suitable for electrical analysis;

15       an analysis generator, coupled to said segmentation generator, operable to electrically analyze said converted segment file in a plurality of calculation programs, executed in sequence, and to create electrical analysis output files;

20       an integrator, coupled to said analysis generator, operable to integrate said analysis output files in controlled sequence into a single model file and to store said file in a report storage; and

25       an output generator, coupled to said integrator, operable to create summary files in specific formats or displays.

2. The system according to Claim 1 wherein said segmentation generator comprises:

30       a segment selector, coupled to said input data

generator, operable to select segments and test lines of said structure to be modeled, select the input data correlated to said segments and test lines, and file said data in an input data storage;

a sequence organizer, coupled to said input data storage, operable to arrange said segments in the correct order for modeling and to create segment data files; and

a model data translator, coupled to said segment data file, operable to read said segment data files, to convert said segment into a format suitable for electrical analysis, and to create an analysis input storage, suitable for electrical analysis to be performed by a plurality of calculation programs executed in sequence.

3. The system according to Claim 2 further comprising a source of parametric data coupled to said sequence organizer.

4. The system according to Claim 1 wherein said electronic structure is an integrated circuit package.

5. The system according to Claim 1 wherein said electronic structure is a semiconductor device comprising an integrated circuit chip assembled in a package.

6. The system according to Claim 1 wherein said electronic structure is an electronic substrate comprising a conductive pattern embedded in an insulating medium.

7. The system according to Claim 1 wherein said electronic structure is a metallic leadframe used in electronic devices.

8. The system according to Claim 1 wherein said electronic structure is a semiconductor device assembled on an electronic substrate.

9. The system according to Claim 1 wherein said analysis generator comprises two-dimensional and three-dimensional electrical calculation means operable to cover all test lines and specified frequencies.
- 5 10. The system according to Claim 1 wherein said analysis generator produces a multitude of patterns comprising electrical resistances, capacitances, and inductances.
11. The system according to Claim 1 further comprising an integrator operable to calculate the scattering
- 10 parameters for electrical high frequency assembly.
- 2 12. A computer system for modeling an electronic structure, comprising:
- an input data generator operable to accept and store graphical, geometric, functional and compositional ✓
  - 15 inputs and to create patterns of electrical conductors and insulators of the structure to be modeled;
  - a segment selector, coupled to said input data generator, operable to select segments and test
  - 20 lines of the structure to be modeled, to select the input data correlated to said segments and ✓  
test lines from said data generator, and to create input data files;
  - a user interface including an operations controller, coupled to said input data files, operable to ✓
  - 25 respond to user commands for controlling execution and timing of a multitude of operations of said computer system;
  - a sequence organizer, coupled to said input data file and further to additional parametric data storage,
  - 30 operable to arrange said segments in the correct order for modeling and to create segment data files;

a model data translator, coupled to said segment data file and further to said operations controller, operable to read said segment data files, to follow instructions from said operations controller, to convert said segment data into a format suitable for electrical analysis, and to store the results in an analysis input storage; an analysis generator, coupled to said analysis input storage and further to said operations controller, operable to execute a plurality of electrical calculation programs in sequence and to create electrical analysis output files; a model data integrator, coupled to said analysis output files and further to said operations controller, operable to integrate said analysis output files in controlled sequence into a single model file and to store said file in a report storage; and an output generator, coupled to said model data integrator and further to said user interface, operable to create said model for each frequency analysis.

13. The computer system according to Claim 12 wherein said output generator creates said model in the format of SPICE-acceptable decks, summary and report files, IBIS - decks, and displays.

14. The computer system according to Claim 12 further comprising a coupling of said parameter data storage to said user interface operable to transmit parametric data to said parameter data storage.

15. The computer system according to Claim 12 wherein said input data generator comprises:

an input unit operable to create patterns of

electrical conductors and insulators;  
a graphical input unit operable to accept, select and  
read geometric and graphical programs; and  
a parametric input unit operable to accept  
5 compositional and functional parameters.

16. The computer system according to Claim 15 wherein said  
geometric and graphical programs are selected from a  
group consisting of pro/ENGINEER and AutoCAD programs.

17. The computer system according to Claim 12 wherein said  
10 input data files, coupled to said segment selector,  
comprise storage of the input data after segment and  
test line selection in a format suitable for the  
selected graphical input program.

18. The computer system according to Claim 17 wherein said  
15 input data storage is in the IGES format for input data  
using Pro/ENGINEER graphics, and in the DXF format for  
input data using AutoCAD graphics.

19. The computer system according to Claim 12 wherein said  
operations controller is operable  
20 to coordinate the sequence of electrical analysis  
calculation programs;  
to instruct the start of the execution of each said  
calculation programs;  
to process signals at the end of each program  
25 execution;  
to control the successful completion of each said  
calculation programs, thereby providing  
acceptance of successful executions and refusal  
of failed executions;  
30 to search for the next said segment in said  
sequence;  
to control the storage of the completed analysis  
results in said analysis output files; and

to instruct their advancement to said model data integrator.

20. The computer system according to Claim 12 wherein said parametric data storage is operable to specify global parameters applicable to said electronic structure to be modeled.

21. The computer system according to Claim 12 wherein said analysis generator comprises electrical computational programs operable

for each electrical frequency;

for each test line of said structure to be modeled that needs two-dimensional analysis;

for each group of test lines of said structure to be modeled forming a three-dimensional segment;

for each test line of said structure to be modeled that needs mixed two-dimensional and three-dimensional analysis; and

for each test line of said structure to be modeled that traverses regions of different dielectric constants, electrical potentials, geometries, or symmetries.

22. A computer-implemented method for constructing a model of an electronic structure, comprising the steps of:

generating input data comprising information

describing a plurality of characteristics of a structure to be modeled, wherein said plurality of characteristics comprise a plurality of segments, subdivision, and compositions;

generating segments of said structure to be modeled,

selecting said segments, organizing the sequence of said segments, storing the data related to said sequenced segments into a segment file, and converting said segment file into a format

suitable for electrical analysis;  
electrically analyzing said converted segment file  
in a plurality of calculation programs executed  
in sequence, and creating electrical analysis  
5 output files;  
integrating said analysis output files in controlled  
sequence into a single model file and storing  
said file in a report storage; and  
creating summary files in specific formats or  
10 displays.

23. The computer-implemented method according to Claim, 22  
wherein said step of generating input data of said  
electronic structure comprises:

creating patterns of electrical conductors and  
15 insulators of said electronic structure;  
accepting, selecting and reading geometric and  
graphical programs describing a plurality of  
characteristics of said electronic structure; and  
accepting compositional and functional parameters of  
20 said electronic structure.

24. The computer-implemented method according to Claim 22  
wherein said step of generating segments comprises:

selecting segments and test lines of said structure  
to be modeled, selecting the input data  
25 correlated to said segments and test lines, and  
filing said data in an input data storage;  
arranging said segments in the correct order for  
modeling, and creating a segment data storage;  
and  
30 converting said segment data storage into a format  
suitable for electrical analysis, and creating an  
analysis input storage suitable for electrical  
analysis to be performed by a plurality of

programs executed in sequence.

25. The computer-implemented method according to Claim 24 wherein said step of converting said segment data storage into a format suitable for electrical analysis comprises:

5       reading the first segment from said segment data storage;  
      identifying all element groups that form a segment by a series of automatic steps comprising:  
10       determining whether the end of the three-dimensional segment has been reached;  
      finding the edges of the geometry in said segment;  
      ordering said edges; and  
15       determining whether the end of the two-dimensional segment has been reached;  
      converting the three-dimensional segment data and the two-dimensional segment data into the input format for electrical analysis;  
20       transferring the data in said input format to the analysis input storage;  
      determining whether another segment is stored in said segment data storage; and  
      repeating all said steps until all segments in said  
25       segment data storage have been read and converted.

26. The computer-implemented method according to Claim 22 wherein said step of integrating said analysis output files into a single model file comprises:
- 30       reading the analysis output as stored in said analysis output files;  
      controlling said analysis output by reading each segment from said segment data storage and



collating a sequence list;  
determining electrical connectivity as based on  
symmetry and branching of said collated sequence  
list;

5           creating analysis output in a suitable format; and  
          storing said analysis output in a report storage.

27. The computer-implemented method according to Claim 26  
      wherein said collated sequence list, based on symmetry  
      and branching, generates a model of a complete  
10       electronic structure.

28. The computer-implemented method according to Claim 26  
      wherein said format of the analysis output is the SPICE-  
      model format.

29. The computer-implemented method according to Claim 26  
15       wherein said analysis output has lumped or distributed  
      format.

30. The computer-implemented method according to Claim 22  
      further comprising the steps of :  
          controlling process operations by responding to user  
20       commands; and  
          automatically controlling execution and timing of a  
          plurality of operations.

31. The computer-implemented method according to Claim 30  
      wherein said step of controlling operations comprises  
25       the steps of:

          coordinating the sequence of the electrical analysis  
          calculation programs;  
          instructing the start of the execution of each said  
          calculation programs;  
30       processing signals at the end of each program  
          execution;  
          controlling the successful completion of each said  
          calculation programs, thereby accepting

successful executions and refusing failed  
executions;

searching for the next said segment in said sequence;  
controlling the storage of the completed analysis

5        results in the analysis output files; and  
instructing the advancement of said results to the  
model data integrator.